

**DECLARATION  
Utility Application**

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled **METHOD AND SYSTEM FOR FACILITATING ELECTRONIC CIRCUIT AND CHIP DESIGN USING REMOTELY LOCATED RESOURCES** the specification of which

(Check One) ☐ is attached hereto OR  
☒ was filed on February 28, 2000 as United States Application Serial No. 09/514,757 or PCT International Application No. \_\_\_\_\_ and was amended on \_\_\_\_\_ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment(s) referred to above.

I acknowledge the duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, § 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate, or § 365(a) of any PCT international application which designated at least one country other than the United States of America, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or of any PCT international application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application Number(s)	Country	Date of Filing	Priority Claimed	
			Yes	No

I hereby claim the benefit under Title 35, United States Code §119(e) of any United States provisional application(s) listed below.

Application Number(s)	Filing Date

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s), or § 365(c) of any PCT international application designating the United States of America, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT international application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.

U.S. Parent Application Number	PCT Parent Number	Parent Filing Date	Status-Patented, Pending or Abandoned

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements are made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Title 18, United States Code, § 1001 and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

201	FULL NAME OF INVENTOR	FIRST Name William	MIDDLE Initial H.	LAST Name ROBERTSON	
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INVENTOR'S SIGNATURE			DATE 6/26/02		

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INVENTOR'S SIGNATURE			DATE		

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

<b>In re the Application of:</b>	)	
	)	<b>Group Art Unit: 2812</b>
William H. Robertson et al.	)	
	)	
<b>Serial No.: 09/514,757</b>	)	
	)	
<b>Filed: February 28, 2000</b>	)	
	)	
<b>For: METHOD AND SYSTEM FOR</b>	)	
<b>FACILITATING ELECTRONIC CIRCUIT</b>	)	
<b>AND CHIP DESIGN USING REMOTELY</b>	)	
<b><u>LOCATED RESOURCES</u></b>	)	

**POWER OF ATTORNEY**  
**AND REVOCATION OF PREVIOUS POWERS**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

Cadence Design Systems, Inc., a Delaware corporation, having a place of business at 2655 Seely Avenue, San Jose, California 95134, certifies that to the best of its knowledge and belief it is the assignee or is entitled to ownership of the entire right, title, and interest in and to the above-referenced patent application by virtue of an Assignment recorded on July 28, 2000 at Reel 011000, Frame 0267, and represents that the undersigned is a representative authorized and empowered to sign on behalf of Cadence Design Systems, Inc., which hereby revokes all powers of attorney previously given and appoints all the attorney(s) and/or agent(s) identified by:

**Customer Number 23639**  
Bingham McCutchen LLP  
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with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith. Send all future correspondence to the attention of Peter C. Mei, Reg. No. 39,768, care of the above address and direct all telephone calls to the same at (650) 849-4870.

Assignee of Interest:

Cadence Design Systems, Inc.

Dated: \_\_\_\_\_

1/15/03

By: \_\_\_\_\_



Name: Peter Chen

Title: Vice President and

Associate General Counsel

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